



Dipartimento di Fisica G. Occhialini
SEMINARI DI DOTTORATO

Aula 05, Edificio U2

13 Settembre 2019

h. 09:30

M. Arosio, Ciclo XXXIV

A dV/dt self-adjusting gate driver architecture for Superjunction switches for motor drive applications

h. 09:50

E. Barteselli, Ciclo XXXIV

MEMS microphones for low power and low voltage applications

h.10:10

S. De Gasperi, Ciclo XXXIV

Horizontal crack detection structures for DMOS power switches

h.10:50

O. Gasparri, Ciclo XXXIV

OTP memory cell in Smart7 - Study and Implementation

h. 11:10

E. Mandelli, Ciclo XXXIV

Dual Level Gate Driver for IGBTs driving optimization