

## Conference presentations

### 1990

- [C.1] A. Baschirotto, R. Castello, and F. Montecchi  
**"Finite gain compensation technique for high-Q bandpass switched-capacitor filters"**  
IEEE International Symposium on Circuits and Systems (ISCAS '90), New Orleans 1990, pag. 2813-2816
- [C.2] R. Castello, F. Montecchi, R. Alini, and A. Baschirotto  
**"A very linear BiCMOS transconductor for high-frequency filtering applications"**  
IEEE International Symposium on Circuits and Systems (ISCAS '90), New Orleans 1990, pag. 1364-1367
- [C.3] A. Baschirotto, M. Cassis, F. Montecchi, G. Palmisano, and D. Rossi  
**"BiCMOS switched-capacitor linear-phase bandpass filter for RDS applications"**  
European Solid-State Circuits Conference (ESSCIRC '90), Grenoble 1990, pag. 185-188

### 1991

- [C.4] A. Baschirotto  
**"IIR double-sampled switched-capacitor building blocks for high-frequency decimators"**  
IEEE International Symposium on Circuits and Systems (ISCAS '91), Singapore 1991, pag. 1673-1676
- [C.5] A. Baschirotto, M. Cassis, P. Kirchlechner, F. Montecchi, G. Palmisano, and D. Rossi  
**"An analog BiCMOS integrated circuit for front-end RDS decoder"**  
IEEE International Conference on Consumer Electronics (ICCE '91), Chicago 1991, pag. 338-339
- [C.6] R. Alini, A. Baschirotto, and R. Castello  
**"8-32 MHz tunable BiCMOS continuous-time filters"**  
European Solid-State Circuits Conference (ESSCIRC '91), Milano 1991, pag. 9-12  
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**'Integrated continuous-time filters'**, edited by Y. Tsividis and J. O. Voorman, IEEE Press, pag. 235-238

### 1992

- [C.7] R. Alini, A. Baschirotto, R. Castello, and F. Montecchi  
**"Accurate MOS threshold voltage detector for bias circuitry"**  
IEEE International Symposium on Circuits and Systems (ISCAS '92), San Diego 1992, pag. 1280-1283
- [C.8] R. Castello, and A. Baschirotto  
**"Analog blocks in telecommunications systems"**  
International Symposium on Signals, Systems and Electronics (ISSSE '92), Paris 1992, pag. 208-213

### 1993

- [C.9] A. Baschirotto, R. Alini, R. Castello, and F. Montecchi  
**"High-speed BiCMOS operational amplifier for switched-capacitor circuits"**  
IEEE International Symposium on Circuits and Systems (ISCAS'93), Chicago 1993, pag. 998-1001
- [C.10] A. Baschirotto, R. Castello, and F. Montecchi  
**"Exact design of high-frequency SC circuits using low-gain opamps"**  
IEEE International Symposium on Circuits and Systems (ISCAS'93), Chicago 1993, pag. 1014-1017
- [C.11] A. Baschirotto, G. Cesura, F. Rezzi, and F. Svelto  
**"A BiCMOS tunable shaper for elementary particle detectors"**  
IEEE International Symposium on Circuits and Systems (ISCAS'93), Chicago 1993, pag. 1077- 1080
- [C.12] A. Baschirotto, R. Alini, R. Castello, and F. Montecchi  
**"Analog BiCMOS continuous-time delay-line with adjustable delay-time"**  
European Conference on Circuits Theory and Design (ECCTD '93), Davos 1993, pag. 1129-1134

- [C.13] A. Baschirotto, M. Bosetti, R. Castello, A. Gola, G. Pessina, P. Rancoita, M. Rattaggi, M. Redaelli, and G. Terzi  
**"A monolithic high-speed bipolar charge sensitive preamplifier for calorimeters applications"**  
European Conference on Circuits Theory and Design (ECCTD '93), Davos 1993, pag. 1141-1146

**1994**

- [C.14] M. Segato, A. Baschirotto, G. Biaggioni, G. Cortelazzo, and E. Malavasi  
**"SC realization of anti-aliasing CCIR-601 video filters"**  
IEEE International Conference on Consumer Electronics (ICCE '94), Chicago 1994, pag. 202-203
- [C.15] A. Baschirotto, M. Bosetti, R. Castello, A. Gola, G. Pessina, P. Rancoita, M. Rattaggi, M. Redaelli, and G. Terzi  
**"High-speed monolithic read-out system for high energy physics experiments"**  
IEEE International Symposium on Circuits and Systems (ISCAS '94), Londra 1994, pag. 107-110
- [C.16] A. Baschirotto, F. Rezzi, and R. Castello  
**"Design of high-frequency BiCMOS continuous-time filters with low-output impedance transconductor"**  
IEEE International Symposium on Circuits and Systems (ISCAS '93), Londra 1994, pag. 665-668
- [C.17] F. Rezzi, A. Baschirotto, and R. Castello  
**"A 3V pseudo-differential transconductor with intrinsic rejection of the common-mode input signal"**  
IEEE Midwest Symposium on Circuits and Systems (MIDWEST '94), Lafayette 1994, August 1994 - pag. 85-88
- [C.18] F. Alberti, A. Baschirotto, G. Brasca, C.M. Milanese, F. Montecchi, and D. Rossi  
**"3V BiCMOS analog front-end for RDS demodulation systems"**  
European Solid-State Circuits Conference (ESSCIRC '94), Ulm 1994, pag. 112-115

**1995**

- [C.19] A. Baschirotto, F. Montecchi and R. Castello  
**"A 150 Ms/s 20mW BiCMOS switched-capacitor biquad using precise gain opamps"**  
IEEE International Solid-State Circuits Conference (ISSCC '95) - San Francisco, February 1995 - Digest of technical papers - pag. 212-213.
- [C.20] A. Baschirotto, V. Liberali, C. Samori  
**"Parallel structure for high-frequency Sigma-Delta modulators"**  
European Conference on Circuits Theory and Design (ECCTD '95), Istambul 1995
- [C.21] F. Rezzi, A. Baschirotto, and R. Castello  
**"3V 12-55MHz BiCMOS continuous-time filter with pseudo-differential structure"**  
European Solid-State Circuits Conference (ESSCIRC '95), Lille 1995, pag. 74-77
- [C.22] A. Baschirotto, R. Castello, A. Gola, G. Pessina, P. G. Rancoita, M. Redaelli, A. Seidman, M. Volpi  
**"Realization of a versatile high-speed bipolar charge sensitive preamplifier for calorimeter applications"**  
European Solid-State Circuits Conference (ESSCIRC '95), Lille 1995, pag. 310-313
- [C.23] A. Baschirotto, R. Castello, G. Pessina, P. G. Rancoita, M. Rattaggi, M. Redaelli, A. Seidman  
**"Design of a hardened fast bipolar monolithic charge sensitive preamplifier"**  
IEEE 18th Convention of electrical and electronics Engineers in Israel - Tel Aviv, March 7-8, 1995, pag. 2.5.5.1-2.5.5.5

**1996**

- [C.24] A. Baschirotto, A. Nagari, F. Montecchi, R. Castello  
**"Design considerations for a 10.7 MHz BiCMOS high-Q double-sampled SC bandpass filter"**  
IEEE International Symposium on Circuits and Systems (ISCAS '96), Atlanta 12-14 May 1996, pag. 433-436

- [C.25] G. Cortelazzo, E. Malavasi, A. Gerosa, and A. Baschirotto  
**"A new stucture for video-rate 2D SC FIR filter"**  
VIII European Signal Processing Conference (EUSIPCO '96) - Trieste (Italy) September 10-13, 1996
- [C.26] L. Schillaci, A. Baschirotto, R. Castello  
**"A 150MHz 5.4mW Track&Hold operated at 3V supply"**  
22nd European Solid State Circuits (ESSCIRC '96) - Neuchatel (Svizzera) - September 17-19, 1996
- [C.27] A. Baschirotto, G. Brasca, F. Montecchi, F. Stefani  
**"A 96dB SNR -75dB THD switched-capacitor filter for 16bit audio codec applications"**  
22nd European Solid State Circuits (ESSCIRC '96) - Neuchatel (Svizzera) - September 17-19, 1996
- [C.28] A. Baschirotto, G. Boella, R. Castello, G. Pessina, P.G. Rancoita  
**"A readout channel with 1nF fully integrated capacitor for AMS space experiment"**  
IEEE International Conference on Electronics, Circuits and Systems (ICECS '96) Rodos (Greece) - October 13-16, 1996

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- [C.29] A. Baschirotto, R. Castello  
**"A 1V 1.8MHz CMOS Switched-opamp SC filter with rail-to-rail output swing"**  
IEEE International Solid-State Circuits Conference (ISSCC '97) - San Francisco, February 1997 - Digest of technical papers - pag. 58-59.
- [C.30] A. Baschirotto, R. Castello  
**"Low-Voltage Switched-Capacitor Filters"**  
**Invited paper** at Europ. Conf. on Circuits Theory and Design (ECCTD '97) - Budapest, August 1997
- [C.31] A. Baschirotto, M. Bianchetti, G. Brasca, P. Cusinato, S. Dalle Feste, G. Gandolfi, F. Montecchi, F. Stefani, N. Serina  
**"A 16bit Digital-to-Analog converter for audio codec applications"**  
Europ. Conf. on Circuits Theory and Design (ECCTD '97) - Budapest, August 1997
- [C.32] A. Baschirotto, R. Castello  
**"A 1V CMOS fully-differential switched-opamp bandpass ΣΔ modulator"**  
23rd European Solid State Circuits (ESSCIRC '97) - Southampton, UK - September 16-18, 1997 - pag. 152-155
- [C.33] A. Baschirotto, G. Boella, R. Castello, G. Frattini, G. Pessina, P.G. Rancoita  
**"A CMOS low power time-to-voltage converter with 3ns resolution for AMS experiments"**  
IV IEEE International Conference on Electronics, Circuits and Systems (ICECS '97) - II Cairo, 15-18 December 1997 - pag. 1189-1192

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- [C.34] A. Baschirotto, R. Castello  
**"1V Switched-capacitor filters"**  
Workshop on Advances in Analog Circuit Design (AACD '98) Copenaghen , 28-30 April 1998 - pag. 3.1-3.13
- [C.35] A. Baschirotto, G. Boella, R. Castello, G. Frattini, G. Pessina, P.G. Rancoita  
**"Fully integrated readout channel with amplitude and time measurement for AMS experiment on ISSA"**  
IEEE International Symposium on Circuits and Systems (ISCAS '98), Monterey (USA) - May 1998 - p. II-220-223
- [C.36] A. Baschirotto, G. Denti, C. Samori  
**"A CMOS sampling data system for IF-to-baseband demodulation and filtering"**  
IEEE International Symposium on Circuits and Systems (ISCAS '98), Monterey (USA) - May 1998 - p. II-69-72
- [C.37] G. Ferri, A. Costa, and A. Baschirotto  
**"A 1.2V rail-to-rail switched buffer"**  
V IEEE International Conference on Electronics, Circuits and Systems (ICECS '98) - Lisbona, Sept. 1998 - pag. 45-48

- [C.38] A. Baschirotto  
**"A four-channel transimpedance amplifier for CD front-end"**  
24th European Solid State Circuits (ESSCIRC '98) - The Hague, NL - September 22-24, 1998 - pag. 480-483
- [C.39] G. Brasca, V. Colonna, P. Cusinato, G. Gandolfi, F. Stefani, D. Tonietto, and A. Baschirotto  
**"A CD analog front-end in BiCMOS technology"**  
24th European Solid State Circuits (ESSCIRC '98) - The Hague, NL - September 22-24, 1998 - pag. 220-223
- [C.40] A. Baschirotto, G. Bollati, A. Fassina, F. Montecchi, F. Stefani  
**"A Q=100 SC bandpass filter"**  
24th European Solid State Circuits (ESSCIRC '98) - The Hague, NL - September 22-24, 1998 - pag. 92-95
- [C.41] A. Baschirotto  
**"A 40MHz CMOS Sample&Hold operating at 1.2V"**  
24th European Solid State Circuits (ESSCIRC '98) - The Hague, NL - September 22-24, 1998 - pag. 248-251

**1999**

- [C.42] F. Severi, A. Baschirotto, and R. Castello  
**"A 200Ms/s 10mW switched-capacitor filter in a 0.5µm CMOS technology "**  
IEEE International Solid-State Circuits Conference (ISSCC '99) - San Francisco, February 1999 - Digest of technical papers - pag. 400-401.
- [C.43] A. Baschirotto, S. Brigati, F. Francesconi , P. Malcovati , D. Tonietto, and F. Maloberti  
**"Modeling Sigma-Delta Modulator Non-Idealities in SIMULINK"**  
IEEE International Symposium on Circuits and Systems (ISCAS '99), Orlando - June 1999 - pag. II-384-387
- [C.44] D. Tonietto, P. Cusinato, F. Stefani, and A. Baschirotto  
**"Considerations for the design of a 10.7MHz bandpass ΣΔ modulator"**  
European Conference on Circuit Theory and Design '99 (ECCTD '99) - Stresa (Italy), 29 August - 2 September 1999 - pag. 5-8
- [C.45] D. Tonietto, P. Cusinato, F. Stefani, and A. Baschirotto  
**"A 3.3V CMOS 10.7MHz 6th-order bandpass ΣΔ modulator with 78dB dynamic range"**  
European Solid-State Circuit Conference '99 (ESSCIRC '99) - September 21-23, 1999, Duisburg (Germany) - pp. 78-81

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- [C.46] A. Baschirotto, D. Bijno, R. Castello, F. Montecchi  
**"A 1V 1.2µW 4th order bandpass switched-opamp SC filter for a cardiac pacer sensing stage"**  
IEEE International Symposium on Circuits and Systems (ISCAS '2000), Ginevra - June 2000 - pag. III-173-176
- [C.47] A. Baschirotto, G. Frattini  
**"A 3.3V CMOS line-driver for serial bus"**  
IEEE International Symposium on Circuits and Systems (ISCAS '2000), Ginevra - June 2000 - pag. IV-457-460
- [C.48] A. Baschirotto, U. Baschirotto, R. Castello  
**"High-frequency CMOS low-power single-branch continuous-time filters"**  
IEEE International Symposium on Circuits and Systems (ISCAS '2000), Ginevra - June 2000 - pag. II.577-580
- [C.49] R. Cappelletti, A. Baschirotto  
**"A versatile low-power Power Line FSK Transceiver"**  
IEEE Custom Integrated Circuits Conference (CICC '2000), Orlando - June 2000 - pag. 323-326
- [C.50] P. Cusinato, F. Stefani, and A. Baschirotto  
**"Power Consumption Reduction in High-Speed ΣΔ Bandpass Modulators"**  
International Symposium on Low Power Electronics Design (ISLPED '00) - Rapallo, July 2000 - pag. 55-60

- [C.51] P. Cusinato, F. Stefani, and A. Baschirotto  
**"A 73dB SFDR 10.7MHz 3.3V CMOS bandpass ΣΔ modulator sampled at 37.05MHz"**  
European Solid-State Circuit Conference '00 (ESSCIRC '00) - Stockholm (Sweden) - 21-23 Sept. 2000 - pag. 80-83

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- [C.52] P. Cusinato, F. Pasolini, F. Stefani, and A. Baschirotto  
**"Saturation Recovery Technique for High-Order Bandpass Switched Capacitor ΣΔ Modulators "**  
IEEE International Conference on Electronics, Circuits and Systems (ICECS '01) - Malta Sept. 2001 - pp. 1085-1088
- [C.53] A. Gola, N. Bagnalasta, P. Bendiscioli, E. Chiesa, S. Delbo', E. Lasalandra, F. Pasolini, M. Tronconi, T. Ungaretti, A. Baschirotto  
**"A MEMS-based rotational accelerometer for HDD applications with 2.5rad/sec 2 resolution and digital output"**  
European Solid State Circuits Conference (ESSCIRC2001) - Villach (Au) - Sept. 2001 - pp. 336-339
- [C.54] L. Lentola, A. Mozzi, A. Neviani, A. Baschirotto  
**"A 1µA Front-End for Pacemaker Atrial Sensing Channels "**  
European Solid State Circuits Conference (ESSCIRC2001) - Villach (Au) - Sept. 2001 - pp. 268-271
- [C.55] M. Annovazzi, V. Colonna, G. Gandolfi, A. Baschirotto  
**"A 98dB 3.3V 28mW-per-channel multibit audio DAC in a standard 0.35µm CMOS technology "**  
European Solid State Circuits Conference (ESSCIRC2001) - Villach (Au) - Sept. 2001 - pp. 540-543
- [C.56] A. Baschirotto  
**"Low-Voltage Switched-Capacitor Filters"**  
Invited paper - NORCHIP 2001 - Kista - Stockholm (Swe) - Nov. 2001

## 2002

- [C.57] A. Gola, F. Pasolini, E. Chiesa, E. Lasalandra, , M. Tronconi, T. Ungaretti, A. Baschirotto  
**"A 2.5rad/sec<sup>2</sup> resolution digital output MEMS-based rotational accelerometer for HDD applications"**  
Asia-Pacific Magnetic Recording Conference - Singapore - Aug. 2002 - pp. CPI-01 - CPI-02
- [C.58] A. Gola, P. Bendiscioli, E. Chiesa, E. Lasalandra, F. Pasolini, M. Tronconi, T. Ungaretti, and A. Baschirotto  
**"A 80dB-SNR ±1g fully-integrated biaxial linear accelerometer in a standard 0.5µm CMOS technology for high-sensitivity applications"**  
European Solid State Circuits Conference (ESSCIRC2002) - Florence (It) - Sept. 2002 - pp. 641-644
- [C.59] G.Gramegna, G. Montagna, I.Bietti, M.Franciotta, A. Baschirotto, G. Abbotto, R.Pelleriti, M.Paparo, R. Castello  
**"A 35-mW - 3.6 mm<sup>2</sup> fully integrated 0.18um CMOS GPS Radio"**  
European Solid State Circuits Conference (ESSCIRC2002) - Florence (It) - Sept. 2002 - pp. 431-434
- [C.60] V. Colonna, G. Gandolfi, F. Stefani, and A. Baschirotto  
**"A 10.7MHz Self-Calibrated SC Multibit 2nd-Order Bandpass ΣΔ Modulator"**  
European Solid State Circuits Conference (ESSCIRC2002) - Florence (It) - Sept. 2002 - pp.575-578

## 2003

- [C.61] C. Meroni, E. Botti, A. Grosso, F. Stefani, M. Ghioni, and A. Baschirotto  
**"A 100dB 4x40W Digital Input Class-AB Power DAC for Audio Applications"**  
IEEE International Solid-State Circuits Conference (ISSCC '03) - San Francisco, February 2003 - Digest of technical papers – pp. 130-131
- [C.62] S. D'Amico, A. Baschirotto  
**"0.18µm CMOS Gm-C filter digitally tuned for telecom receivers"**  
IEEE International Symposium on Circuits and Systems (ISCAS '03), Bangkok - May 2003 - Vol. I - pp. 493-496

- [C.63] F. Adduci, M. Annovazzi, G. Boarin, A. Colaci, V. Colonna, G. Gandolfi, M. Sala, F. Salidu, F. Stefani, M. Frey, P. Kirchlechner, C. Kutschenreiter, A. Baschirotto  
**"A DSP-Based Digital IF AM/FM Car-Radio Receiver"**,  
XXIX European Solid State Circuits Conference (ESSCIRC2003) - Estoril (Portugal) - Sept. 2003 - pp. 201-204
- [C.64] S. D'Amico, and A. Baschirotto  
**"An active-Gm-RC continuous-time biquad low-pass cell"**  
IEEE International Conference on Electronics (ICECS 2003) - Dubai - Dec. 2003 - pp. 251-254

## 2004

- [C.65] A. Baschirotto, E. Dallago, P. Malcovati, M. Marchesi, G. Venchi  
**"Design and Characterization of a family of Fluxgate Magnetic Sensors in PCB Technology"**  
The 9<sup>th</sup> National Conference on Sensor and Microsystems (AISEM 2004) - Ferrara (Italy) - February 2004 - pp. 193, published by World Scientific Press
- [C.66] F. Corsi, C. Marzocca, G. Matarrese, A. Baschirotto, S. D'Amico  
**"Pseudo-random Sequence Based Tuning System for Continuous-time Filters"**  
Design and Automatic Testing Conference - (DATE 2004) - Paris (France) - February 2004 – Vol. 1, pp. 16-20
- [C.67] A. Baschirotto, M. Sala, F. Salidu, F. Stefani  
**"IF A/D Converters for a DSP-based FM-receiver"**  
Workshop on Advances in Analog Circuit Design (AACD 2004) - Montreux (Switzerland) - 6-8 April 2004
- [C.68] S. D'Amico, A. Baschirotto  
**"Low-Power Compact Gm-C Filters Structures"**  
IEEE International Symposium on Circuits and Systems (ISCAS '04), Vancouver (Canada) - May 2004 - pp. I-873–I-876
- [C.69] S. D'Amico, A. Baschirotto  
**"A 0.13µm CMOS low-voltage reconfigurable filter for UMTS/WLAN transmitters"**  
IEEE Workshop on Wireless Circuits and Systems (WoWCAS), May 21 and 22, 2004, Vancouver, Canada - pp. 12-13
- [C.70] A. Baschirotto, E. Dallago, P. Malcovati, M. Marchesi, G. Venchi  
**"Fluxgate magnetic sensor in PCB technology"**  
IEEE Instrumentation and Measurement Technology Conference (IMTC 2004), Como, Italy, 18-20 May 2004 - pp. 808-812
- [C.71] S. D'Amico, V. Giannini, A. Baschirotto  
**"An algorithm for the automatic design and the power optimization of Active-Gm-RC continuous-time biquadratic cell"**  
2<sup>nd</sup> IEEE Northeast Workshop on Circuits and Systems (IEEE NEWCAS'04) - June 2004 – pp. 333-336
- [C.72] N. Ghittori, A. Vigna, P. Malcovati, A. Baschirotto  
**"Design trade-offs for a 10bit, 80MHz current steering digital-to-analog converter"**  
2<sup>nd</sup> IEEE Northeast Workshop on Circuits and Systems (IEEE NEWCAS'04) - June 2004 - pp. 249 - 252
- [C.73] V. Colonna, M. Annovazzi, G. Boarin, G. Gandolfi, F. Stefani, A. Baschirotto  
**"A 0.22mm<sup>2</sup> 7.25mW per-channel audio stereo-DAC with 97dB-DR and 39dB SNRout"**  
XXX European Solid State Circuits Conference (ESSCIRC2004) - Leuven (Belgium) - Sept. 2004 - pp. 155-158
- [C.74] M. Grassi, P. Malcovati and A. Baschirotto  
**"A high-precision wide-range front-end for resistive gas sensor arrays"**  
EuroSensors 2004 - Roma (Italy) - Sept. 2004 - pp. 409-410
- [C.75] A. Baschirotto, E. Dallago, P. Malcovati, M. Marchesi, G. Venchi  
**"Precise vector-2D magnetic field sensor system for electronic compass"**  
IEEESensors 2004 - Vienna (Austria) - Oct. 2004 - pp. 1028-1031
- [C.76] S. D'Amico, V. Giannini, and A. Baschirotto  
**"A low-power reconfigurable continuous-time filter for UMTS/WLAN receivers"**  
Norchip 2004 – OSLO – October, 8-10, 2004 – pp. 179-182

- [C.77] S. D'Amico, A. Baschirotto, A. Vigna, N. Ghittori, P. Malcovati  
**"Low-power reconfigurable baseband block for UMTS/WLAN transmitters"**  
Norchip 2004 – OSLO – October 8-10, 2004 – pp. 103-106
- 2005**
- [C.78] M. Grassi, P. Malcovati, G. De Iaco, A. Baschirotto  
**"An integrated wide-range resistance-to-time converter with decoupled oscillator"**  
National Conference on Sensor and Microsystems (AISEM 2005), Firenze, February, 15-17, 2005
- [C.79] A. Baschirotto, M. Grassi, P. Malcovati, C. Di Natale, E. Martinelli, G. Ferri, N. Guerrini, L. Franciosi, S. Capone, M. Prato, P. Siciliano  
**"High-performance blocks for a large dynamic range gas sensor – A PRIN Project"**  
National Conference on Sensor and Microsystems (AISEM 2005), Firenze, February, 15-17, 2005
- [C.80] A. Baschirotto, E. Dallago, P. Malcovati, M. Marchesi, G. Venchi  
**"Micro-integrated double axis planar fluxgate"**  
National Conference on Sensor and Microsystems (AISEM 2005), Firenze, February, 15-17, 2005 - published by World Scientific Press
- [C.81] A. Baschirotto, S. D'Amico, P. Malcovati  
**"Low-Voltage Low-Power Basic Circuits"**  
Invited at Workshop on Advances in Analog Circuit Design (AACD 2005), Limerick (Ireland), April, 5-7, 2005
- [C.82] A. Baschirotto, E. Dallago, P. Malcovati, M. Marchesi, G. Venchi  
**"From a PCB Fluxgate to an integrated micro Fluxgate magnetic sensor"**  
IEEE Instrumentation and Measurement Technology Conference (IMTC 2005), Ottawa, May, 17-19, 2005, pp. 1756-1760
- [C.83] N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschirotto  
**"Behavioral analysis and dimensioning of UMTS transmitter baseband blocks"**  
International Symposium on Circuits and Systems (ISCAS 05), Kobe, Japan, May 23-26, 2005, pp. 388-391
- [C.84] N. Ghittori, P. Malcovati, A. Vigna, A. Baschirotto, S. D'Amico  
**"A Low-Distortion 1.2 V DAC+Filter for Transmitters in Wireless Applications"**  
International Symposium on Circuits and Systems (ISCAS 05), Kobe, Japan, May 23-26, 2005, pp. 776-779
- [C.85] J. H. Nielsen, P. Malcovati, A. Baschirotto and P. Andreani  
**"Technology scaling impact on embedded ADC design for telecom receivers"**  
International Symposium on Circuits and Systems (ISCAS 05), Kobe, Japan, May 23-26, 2005, pp. 4614-4617
- [C.86] M. Grassi, P. Malcovati, A. Baschirotto  
**"Flexible High-Accuracy Wide-Range Gas Sensor Interface for Portable Environmental Nosing Purpose"**  
International Symposium on Circuits and Systems (ISCAS 05), Kobe, Japan, May 23-26, 2005, pp. 5385-5388
- [C.87] N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschirotto  
**"A low-power, low-voltage (11mW/8.4mW, 1.2V) DAC+filter for multistandard (WLAN/UMTS) transmitters"**  
VLSI Symposium 2005 – Kyoto (Japan) – June, 16-18, 2005 – p. 334-337
- [C.88] G. Matarrese, C. Marzocca, S. D'Amico, F. Corsi, A. Baschirotto  
**On chip self-tuning of high performance filters via pseudo-random input test signal**  
11th Annual International Mixed-Signals Testing Workshop (IMSTW2005) – Cannes (France) – June, 27-29, 2005
- [C.89] N. Ghittori, A. Vigna, P. Malcovati, S. D'Amico, A. Baschirotto  
**"A low-voltage, low-distortion (1.2V, 29.5dBm OIP3) reconfigurable baseband block for mobile applications"**  
Ph.D. Research In Micro-Electronics & Electronics Conference (PRIME 2005) – Lausanne (Switzerland) – July, 25-28, 2005, pp. 51-54

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